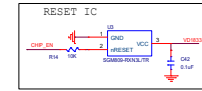
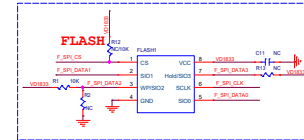
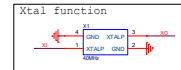


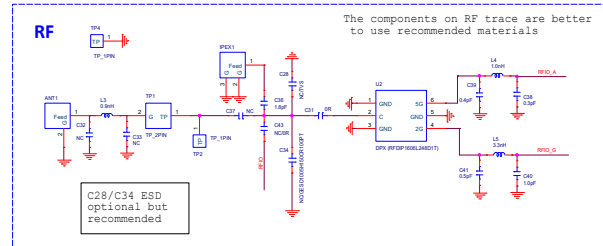
(The Capacitance of AVCC/AVCC_DRIV (pin2/88) should be 1uF)
(The Capacitance of AUDIO_VREF (pin3) should be 4.7nF)

(Pin 69 SAW_LX GND) should connect to (Pin68 GNDSPS)

(The Capacitance of VD11 CORE (pin46/83) & VD1833_SPS (pin70) should be 2.2uF)
(The Capacitance of PSRAM (pin66) should be 4.7uF)

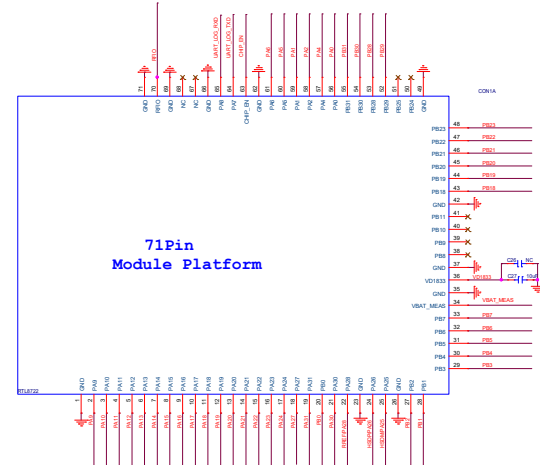


RTL8722DM-QFN38



The components on RF trace are better to use recommended materials

For 3.3V application, if the power up slew rate from 1.5V to 3.0V is slower than 15ms, The wide range vcc Flash is needed or add reset IC.
In the process of power off to power on, if the power off voltage can't be guaranteed to drop below 0.3v and power on again, reset IC should be added



Pin	Signal	Pin	Signal
1	AVCC	39	AVCC
2	AVCC_DRIV	40	AVCC
3	AUDIO_VREF	41	AVCC
4	AVCC	42	AVCC
5	AVCC	43	AVCC
6	AVCC	44	AVCC
7	AVCC	45	AVCC
8	AVCC	46	AVCC
9	AVCC	47	AVCC
10	AVCC	48	AVCC
11	AVCC	49	AVCC
12	AVCC	50	AVCC
13	AVCC	51	AVCC
14	AVCC	52	AVCC
15	AVCC	53	AVCC
16	AVCC	54	AVCC
17	AVCC	55	AVCC
18	AVCC	56	AVCC
19	AVCC	57	AVCC
20	AVCC	58	AVCC
21	AVCC	59	AVCC
22	AVCC	60	AVCC
23	AVCC	61	AVCC
24	AVCC	62	AVCC
25	AVCC	63	AVCC
26	AVCC	64	AVCC
27	AVCC	65	AVCC
28	AVCC	66	AVCC
29	AVCC	67	AVCC
30	AVCC	68	AVCC
31	AVCC	69	AVCC
32	AVCC	70	AVCC
33	AVCC	71	AVCC
34	AVCC	72	AVCC
35	AVCC	73	AVCC
36	AVCC	74	AVCC
37	AVCC	75	AVCC
38	AVCC	76	AVCC
39	AVCC	77	AVCC
40	AVCC	78	AVCC
41	AVCC	79	AVCC
42	AVCC	80	AVCC
43	AVCC	81	AVCC
44	AVCC	82	AVCC
45	AVCC	83	AVCC
46	AVCC	84	AVCC
47	AVCC	85	AVCC
48	AVCC	86	AVCC
49	AVCC	87	AVCC
50	AVCC	88	AVCC
51	AVCC	89	AVCC
52	AVCC	90	AVCC
53	AVCC	91	AVCC
54	AVCC	92	AVCC
55	AVCC	93	AVCC
56	AVCC	94	AVCC
57	AVCC	95	AVCC
58	AVCC	96	AVCC
59	AVCC	97	AVCC
60	AVCC	98	AVCC
61	AVCC	99	AVCC
62	AVCC	100	AVCC